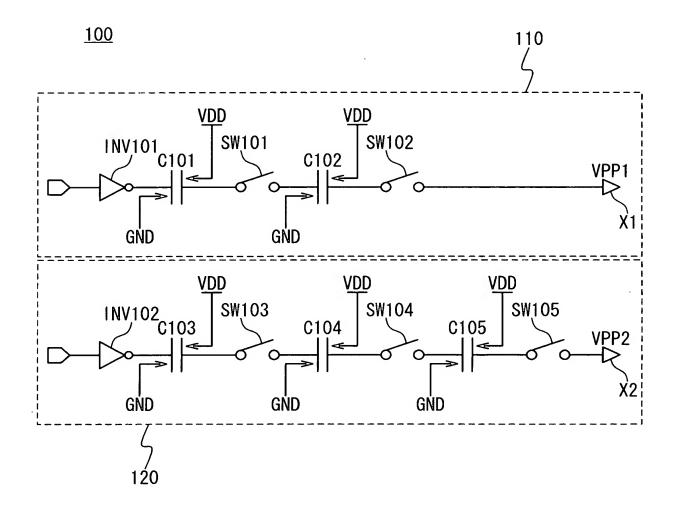
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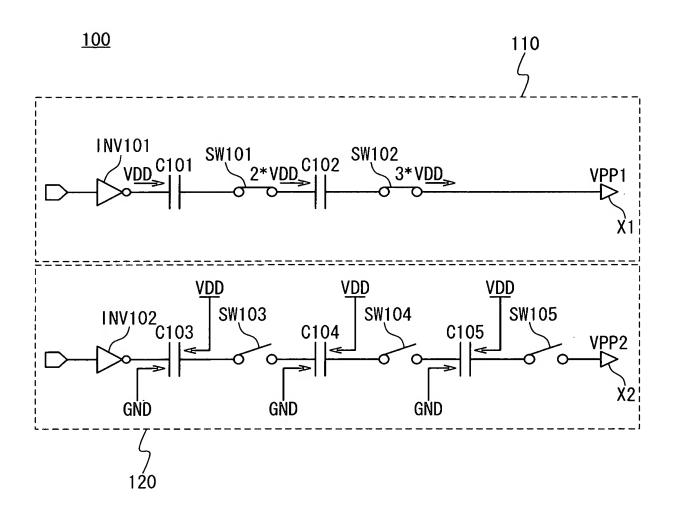
## Fig. 1 PRIOR ART



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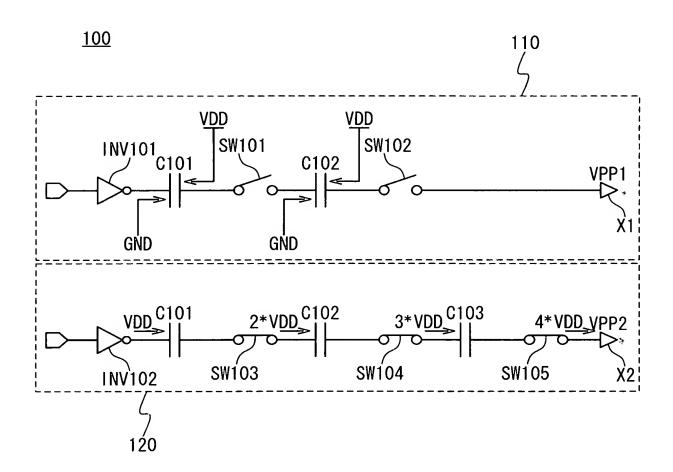
# Fig. 2 PRIOR ART



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## Fig. 3 PRIOR ART

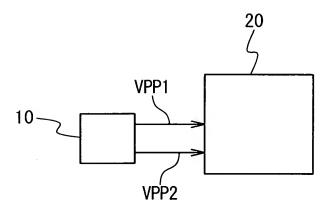


Title: BOOSTING CIRCUIT AND SEMICONDUCTOR DEVICE USING THE SAME

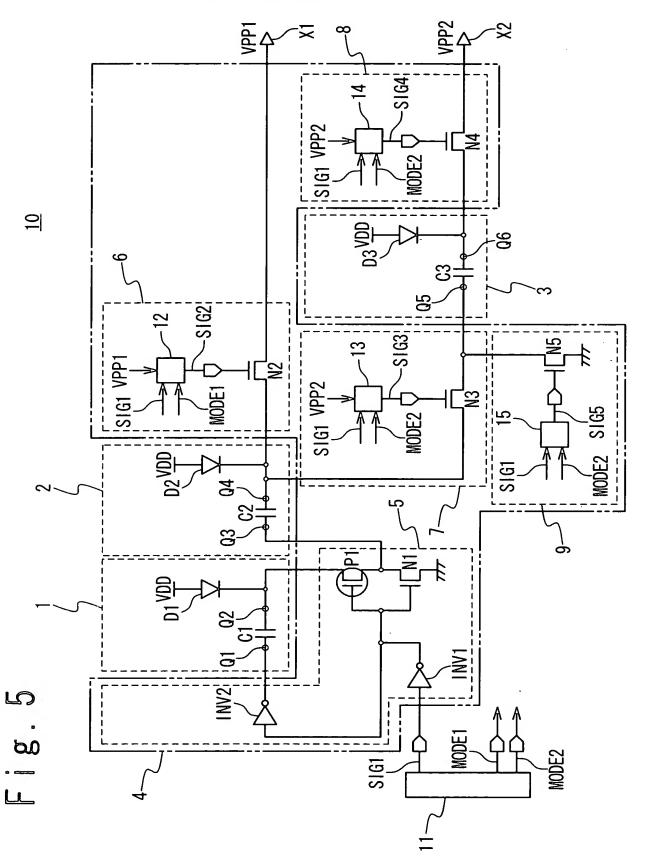
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Fig. 4



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Fig. 6

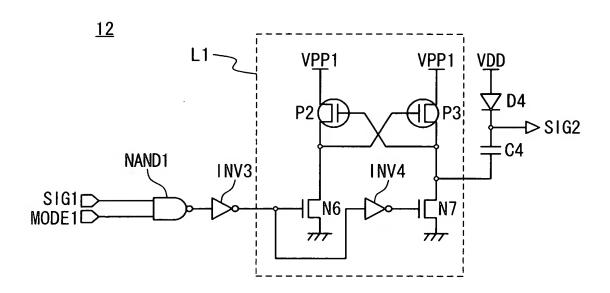
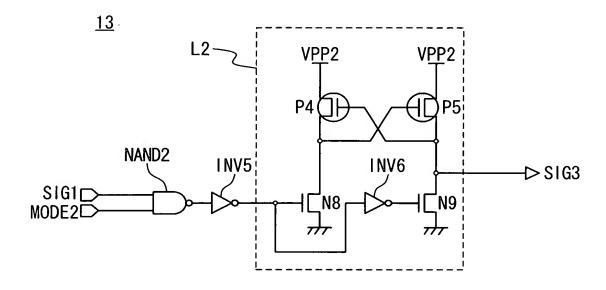


Fig. 7



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Fig. 8

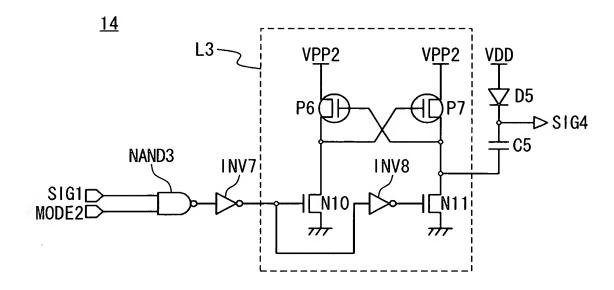
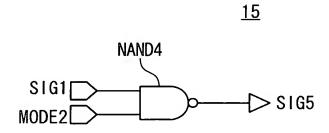


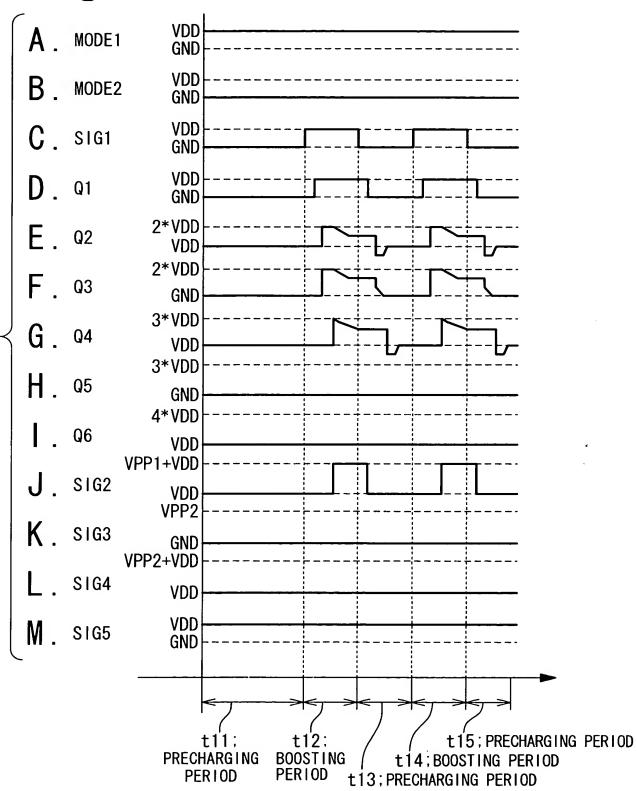
Fig. 9



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#### Fig. 10



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#### Fig. 11

